Old Company Name in Catalogs and Other Documents

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Renesas Electronics website: http://www.renesas.com

April 1st, 2010 Renesas Electronics Corporation

Issued by: Renesas Electronics Corporation (http://www.renesas.com)

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RENESAS

H7N1002LD, H7N1002LS, H7N1002LM

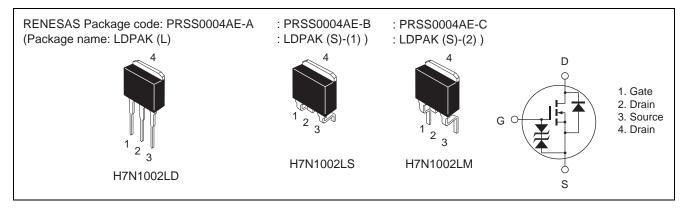
Silicon N Channel MOS FET **High Speed Power Switching**

REJ03G1131-0800 Rev.8.00 Nov 13, 2009

Features

- Low on-resistance
- $R_{DS (on)} = 8 m\Omega typ.$
- Low drive current
- Available for 4.5 V gate drive

Outline



Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Value	Unit
Drain to source voltage	V _{DSS}	100	V
Gate to source voltage	V _{GSS}	±20	V
Drain current	ID	75	A
Drain peak current	I _{D (pulse)} Note 1	300	A
Body to drain diode reverse drain current	I _{DR}	75	A
Avalanche current	I _{AP} Note 3	50	A
Avalanche energy	E _{AR} Note 3	166	mJ
Channel dissipation	Pch Note 2	100	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	٥C

Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

2. Value at Tc = 25°C

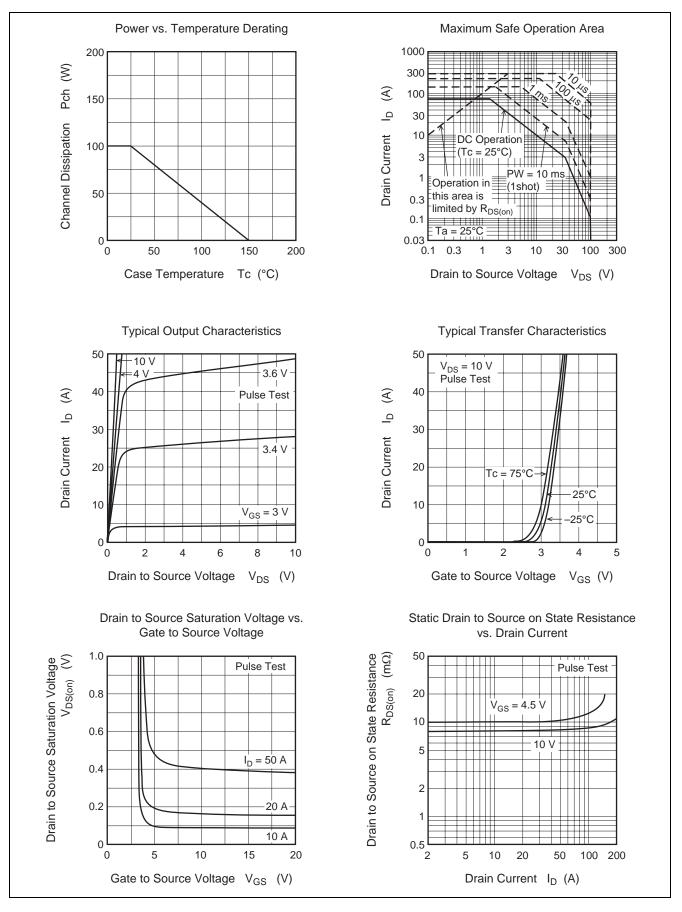
3. Value at Tch = 25° C, Rg $\geq 50 \Omega$

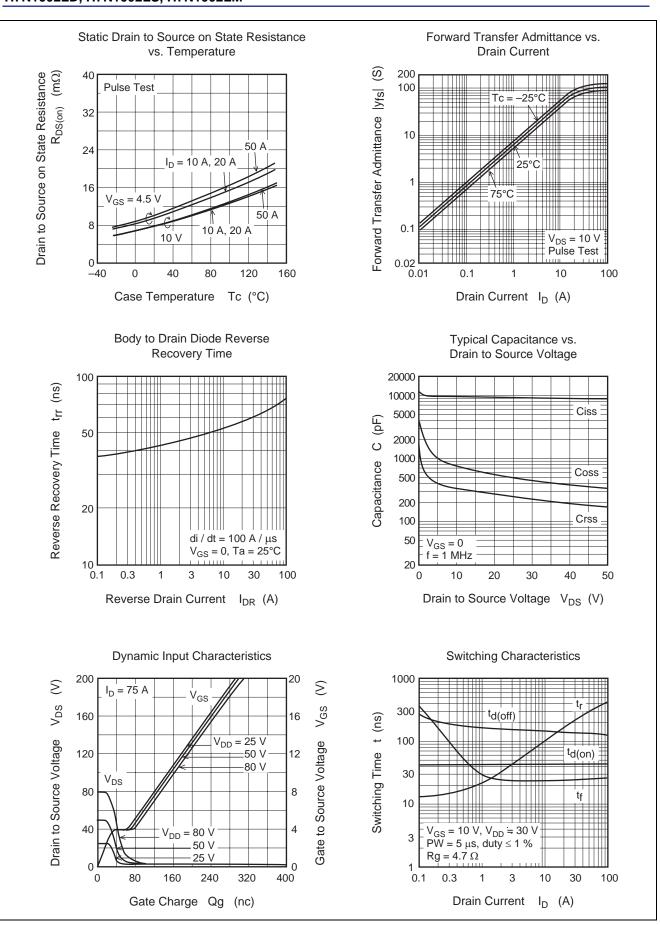
Electrical Characteristics

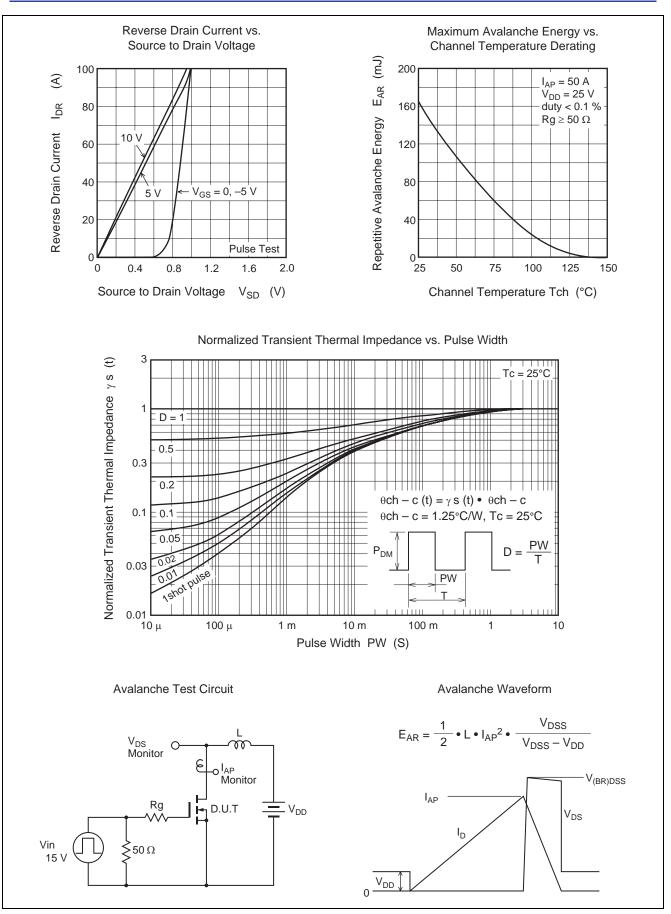
						$(Ta = 25^{\circ}C)$
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V (BR) DSS	100	—	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Gate to source breakdown voltage	V (BR) GSS	±20	—	_	V	$I_G = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I _{GSS}	—	_	±10	μΑ	$V_{GS}=\pm 16~V,~V_{DS}=0$
Zero gate voltage drain current	I _{DSS}	—	—	10	μA	$V_{DS} = 100 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	V _{GS (off)}	1.5	—	2.5	V	$I_D = 1 \text{ mA}, V_{DS} = 10 \text{ V}^{Note 4}$
Static drain to source on state	R _{DS (on)}	—	8	10	mΩ	$I_D = 37.5 \text{ A}, V_{GS} = 10 \text{ V}^{Note 4}$
resistance		—	10	15	mΩ	$I_D = 37.5 \text{ A}, V_{GS} = 4.5 \text{ V}^{\text{Note 4}}$
Forward transfer admittance	y _{fs}	57	95	_	S	$I_D = 37.5 \text{ A}, V_{DS} = 10 \text{ V}^{Note 4}$
Input capacitance	Ciss	—	9700	_	pF	V _{DS} = 10 V
Output capacitance	Coss	_	740	—	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	—	330	_	pF	f = 1 MHz
Total gate charge	Qg	—	155	_	nC	V _{DD} = 50 V
Gate to source charge	Qgs	—	35	_	nC	V _{GS} = 10 V
Gate to drain charge	Qgd	—	33	_	nC	I _D = 75 A
Turn-on delay time	t _{d (on)}	—	43	_	ns	V_{GS} = 10 V, I_D = 37.5 A
Rise time	tr	—	245	_	ns	$R_L = 0.8 \Omega$
Turn-off delay time	t _{d (off)}	—	130		ns	Rg = 4.7 Ω
Fall time	t _f	—	25		ns	
Body to drain diode forward voltage	V_{DF}	—	0.93		V	$I_F = 75 \text{ A}, V_{GS} = 0$
Body to drain diode reverse recovery	t _{rr}	—	70		ns	$I_F = 75 \text{ A}, V_{GS} = 0$
time						di _F /dt = 100 A/µs

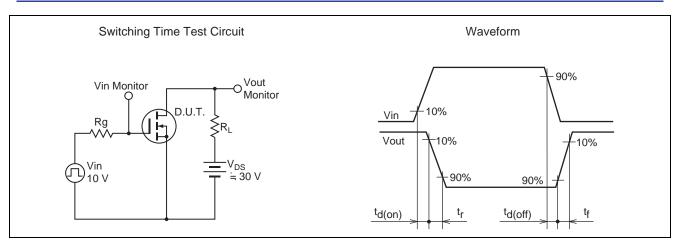
Note: 4. Pulse test

Main Characteristics

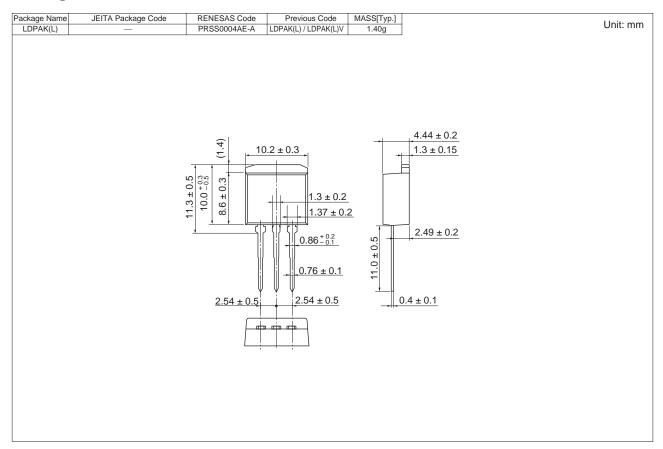


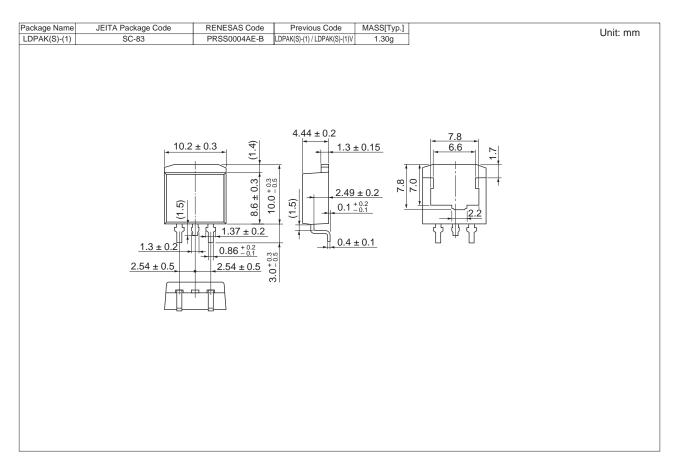




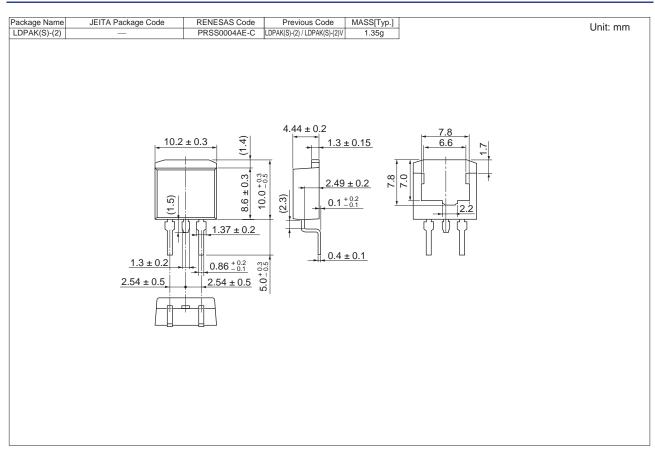


Package Dimensions





H7N1002LD, H7N1002LS, H7N1002LM



Ordering Information

Part Name	Quantity	Shipping Container
H7N1002LD-E	500 pcs	Box (Conductive Sack)
H7N1002LSTL-E	1000 pcs	Taping
H7N1002LMTL-E	1000 pcs	Taping

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